



CS98-106/7/8C

Application No. 09/817,473

August 3, 2003

TO: Commissioner of Patents
P.O. Box 1435
Alexandria, VA 22313-1450

FROM: George O. Saile, Reg. No. 19,572
28 Davis Ave.
Poughkeepsie, N.Y. 12603

SUBJECT: Serial #: 09/817,473
File Date: 03/26/2001
Inventor: YI, XU ET AL.
Examiner: NGUYEN, THANH T.
Art Unit: 2813
Title: GAP FILLING PROCESS IN INTEGRATED
CIRCUITS USING LOW DIELECTRIC CONSTANT
MATERIALS

TECHNOLOGY CENTER 2800

AUG 11 2003

RECEIVED

Entered
9/12/03
Ce.M.

RESPONSE TO OFFICE ACTION

This is in response to the Office Action dated June 3, 2003,
Paper No. 11. Please consider the following regarding the above
referenced application.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited
with the United States Postal Service as first class mail in an
envelope addressed to: Commissioner of Patents, P.O. Box 1435,
Alexandria, VA 22313-1450 on August 5, 2003.

Signature George O. Saile Date: August 5, 2003

George O. Saile, Reg. No. 19,572

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